

## EAST Search History

## EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	"20080116520"	US-PGPUB; USPAT	OR	OFF	2011/05/21 16:54
L2	4	("5557127", "5043781", "6462376", "6407413").pn.	US-PGPUB; USPAT	OR	OFF	2011/05/21 17:07
L3	50	((IGBT\$1 MOSFET\$1 power near2 device\$1 (vertical near2 (channel\$1 transistor \$1))) and (terminat\$4 edge\$1 peripher\$3) with zener near15 gate	US-PGPUB; USPAT	OR	OFF	2011/05/21 18:22
L4	9	((IGBT\$1 MOSFET\$1 power near2 device\$1 (vertical near2 (channel\$1 transistor \$1))) and (terminat\$4 edge\$1 peripher\$3) with zener near15 gate	EPO; JPO; DERWENT	OR	OFF	2011/05/21 18:23
L5	25	3 and (@ad<"20040521" @rlad<"20040521")	US-PGPUB; USPAT	OR	OFF	2011/05/21 18:28
L6	25	3 not 5	US-PGPUB; USPAT	OR	OFF	2011/05/21 18:44
L7	1	("20020050602").PN.	US-PGPUB; USPAT	OR	OFF	2011/05/21 18:56
L8	51	257/452.ccls.	EPO; JPO; DERWENT	OR	OFF	2011/05/21 19:50
L9	0	8 and zener	EPO; JPO; DERWENT	OR	OFF	2011/05/21 19:50
L10	140	257/452.ccls.	US-PGPUB; USPAT	OR	OFF	2011/05/21 19:51
L11	0	10 and zener	EPO; JPO; DERWENT	OR	OFF	2011/05/21 19:51
L12	150	257/106.ccls.	US-PGPUB; USPAT	OR	OFF	2011/05/21 19:58
L13	7	12 and (terminat\$4 edge\$1 peripher\$3 protection) with zener near15 gate	US-PGPUB; USPAT	OR	OFF	2011/05/21 19:59
L14	6395	257/106,146,171,173,334,339,341,355,356,360,544,551.ccls.	US-PGPUB; USPAT	OR	OFF	2011/05/21 20:24
L15	174	257/E21.05,E21.051.ccls.	US-PGPUB; USPAT	OR	OFF	2011/05/21 20:24

L16	33	257/E21.05.ccls.	US-PGPUB; USPAT	OR	OFF	2011/05/21 20:25
L17	2419	257/E29.201,E29.257.ccls.	US-PGPUB; USPAT	OR	OFF	2011/05/21 20:25
L18	1746	257/E27.016,E27.022.ccls.	US-PGPUB; USPAT	OR	OFF	2011/05/21 20:26
L19	111	257/494.ccls.	US-PGPUB; USPAT	OR	OFF	2011/05/21 20:26
L20	90	14 and (terminat\$4 edge\$1 peripher\$3 protection) with zener near15 gate	US-PGPUB; USPAT	OR	OFF	2011/05/21 20:27
L21	0	15 and (terminat\$4 edge\$1 peripher\$3 protection) with zener near15 gate	US-PGPUB; USPAT	OR	OFF	2011/05/21 20:27
L22	27	17 and (terminat\$4 edge\$1 peripher\$3 protection) with zener near15 gate	US-PGPUB; USPAT	OR	OFF	2011/05/21 20:28
L23	32	18 and (terminat\$4 edge\$1 peripher\$3 protection) with zener near15 gate	US-PGPUB; USPAT	OR	OFF	2011/05/21 20:28
L24	46	raymond near grover.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2011/05/21 20:29
L25	152235	terminat\$4 with (device\$1 structure\$1 integrated adj circuit IC die chip)	US-PGPUB; USPAT	OR	OFF	2011/05/21 20:36
L26	20	3 and 25	US-PGPUB; USPAT	OR	OFF	2011/05/21 20:36

5/ 21/ 2011 10:14:25 PM

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